

REMARKS

Reconsideration and allowance of the subject patent application are respectfully requested.

Amendments of a formal nature have been made to claims 1 and 17. These amendments are not made for reasons relating to patentability.

Applicants acknowledge with appreciation the indication that claims 8-17 are allowed.

Claims 3, 4, 6 and 7 were rejected under 35 U.S.C. Section 112, first paragraph, as allegedly failing to comply with the enablement requirement. Specifically, the office action alleges that the specification fails to disclose an embodiment in which "a whole spectrum inverter" and "a partial spectrum inverter" are employed together for retransmission of received signals from a plurality of handsets. Applicants traverse this rejection. In addition to the original claims themselves, Applicants respectfully submit that the third embodiment discussed beginning on page 29 of the subject application describes and fully enables an example embodiment in which "a whole spectrum inverter" and "a partial spectrum inverter" are employed together.

As noted in the paragraph beginning on page 25, line 13, when the frequency of the local oscillator is 258 MHz as shown in Figure 4, "the frequency spectrum is inverted (whole spectrum inversion) at the stage of intermediate frequency ...". Thus, the receiver of Figure 4 (i.e., amplifier 23, BPF 24, frequency converter 25, BPF 26 and IF amplifier 27) functions as a whole spectrum inverter. See also page 26, lines 4-8. Figure 7 relates to a third example embodiment and shows an example implementation of the filter 91 in Figure 4 which includes partial spectrum inverters 105 and 106.

As such, the subject patent application as originally filed describes and enables an example embodiment including a whole spectrum inverter (provided by the receiver supplied with 258 MHz by the local oscillator in Figure 4) and a partial spectrum inverter (e.g., element 105 or 106 in Figure 7), as specified in claim 3.

Applicants also respectfully submit that the specification discloses and enables the features of the dependent claims. The concept of the whole spectrum inverter and/or the partial spectrum inverter involving "arithmetic signal processors" (claims 4 and 6) is described, for example, at page 10, lines 1-3 of the specification. The concept of the partial spectrum inverter

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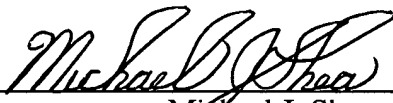
being provided before the whole spectrum inverter (claim 7) is described, for example, at page 33, lines 5-10 of the specification.

For these reasons, Applicants respectfully submit that the Section 112, first paragraph, rejection of claims 3, 4, 6 and 7 is improper and should be withdrawn.

Consequently, this application is now believed to be allowable and favorable office action is respectfully requested.

Respectfully submitted,

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